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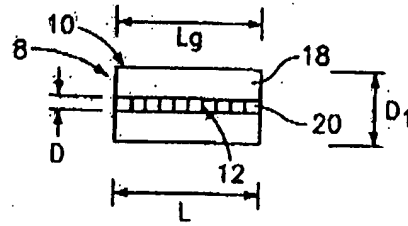


FIG. 1

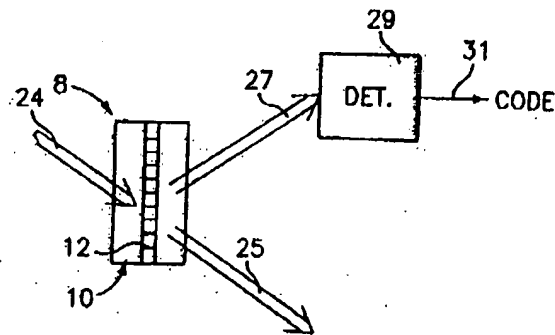


FIG. 2

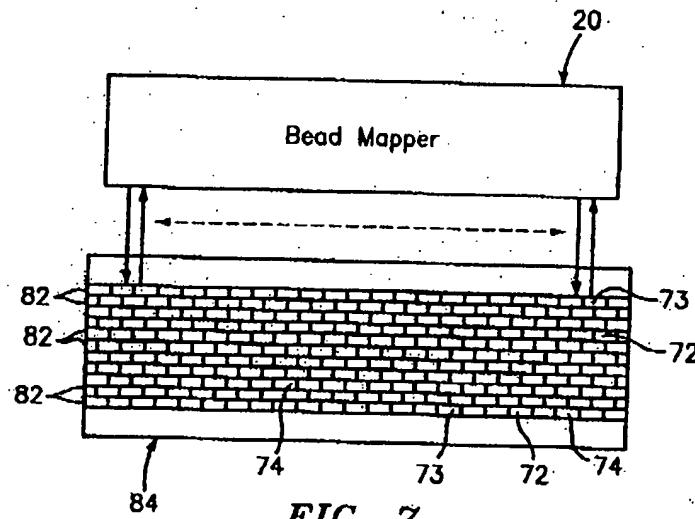


FIG. 7

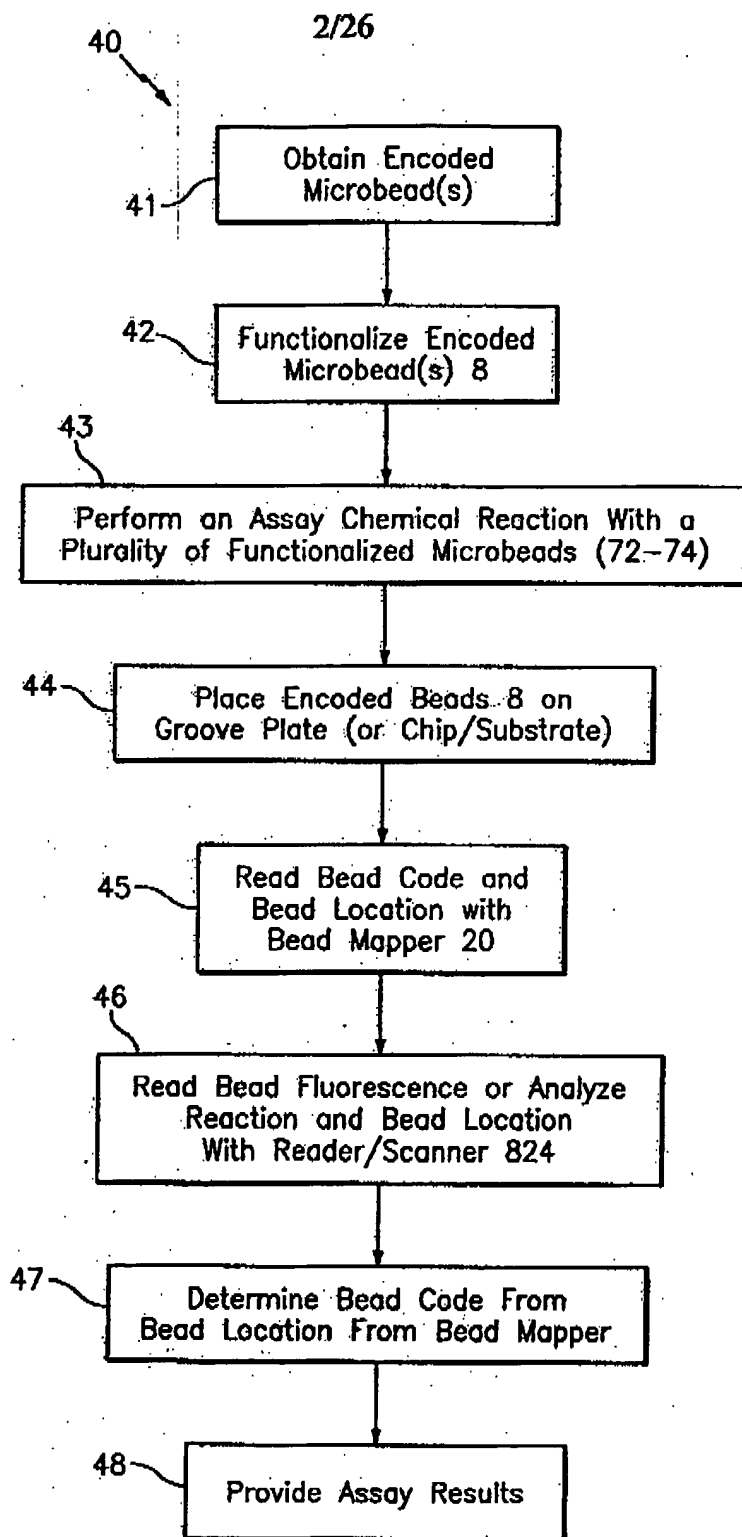


FIG. 3

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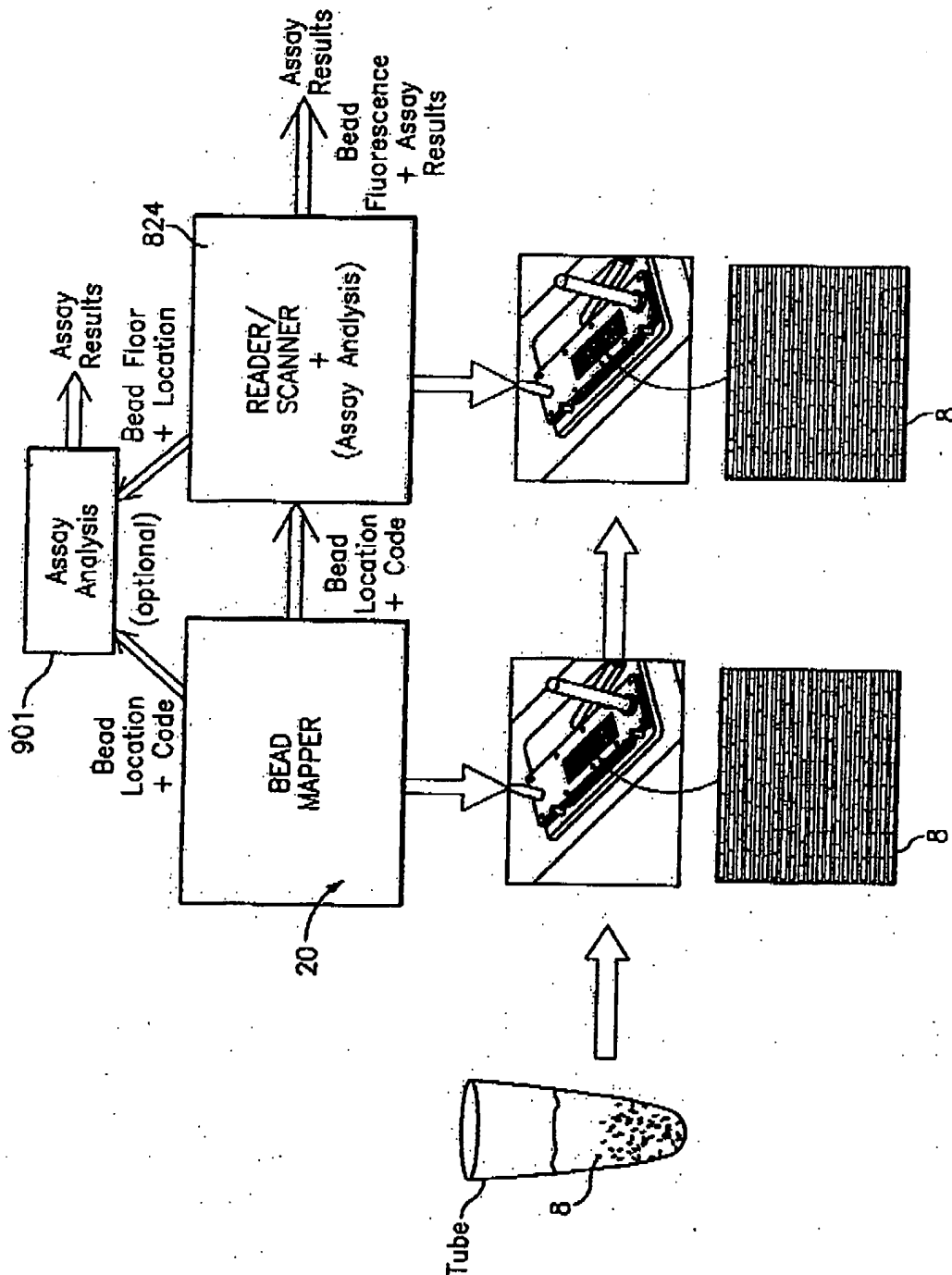


FIG. 3(a)

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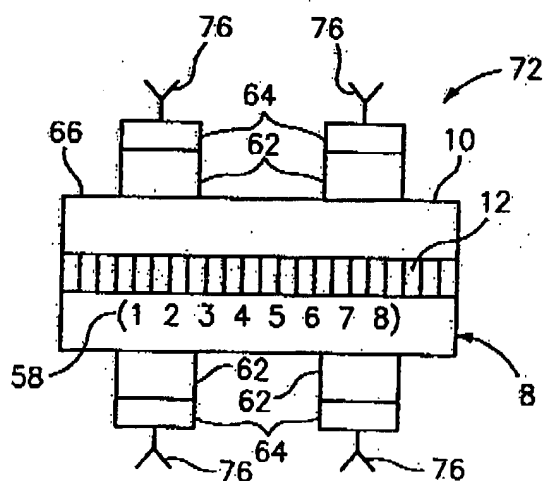


FIG. 4

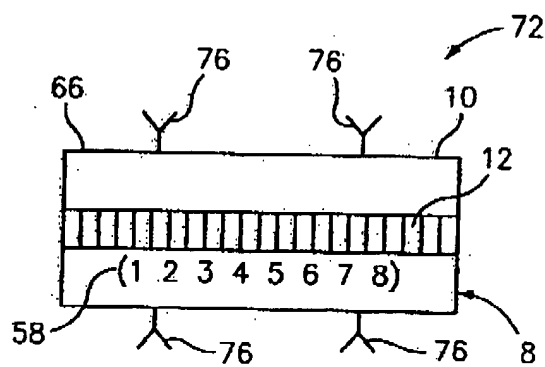


FIG. 5

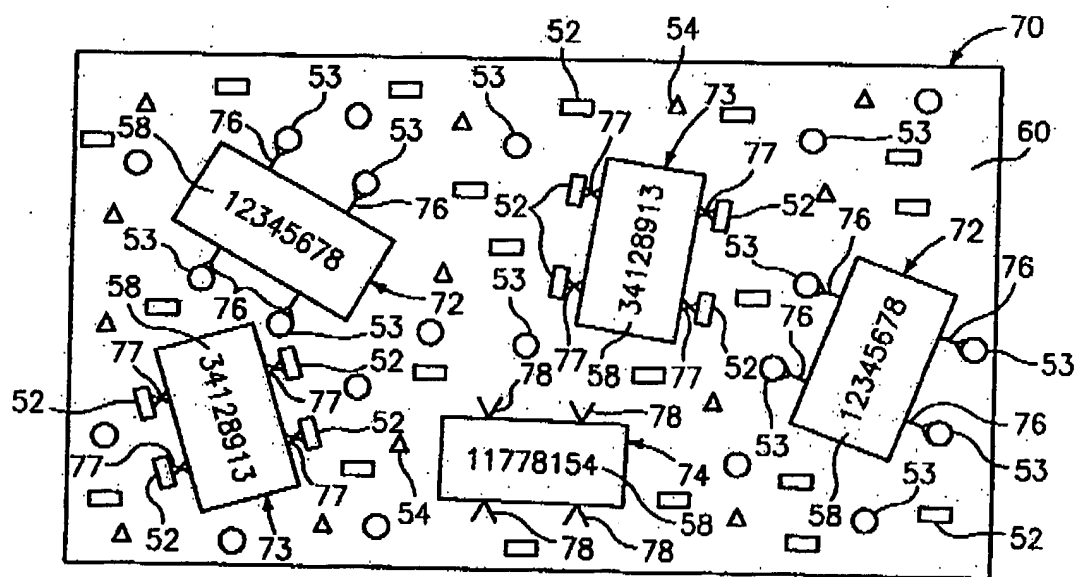
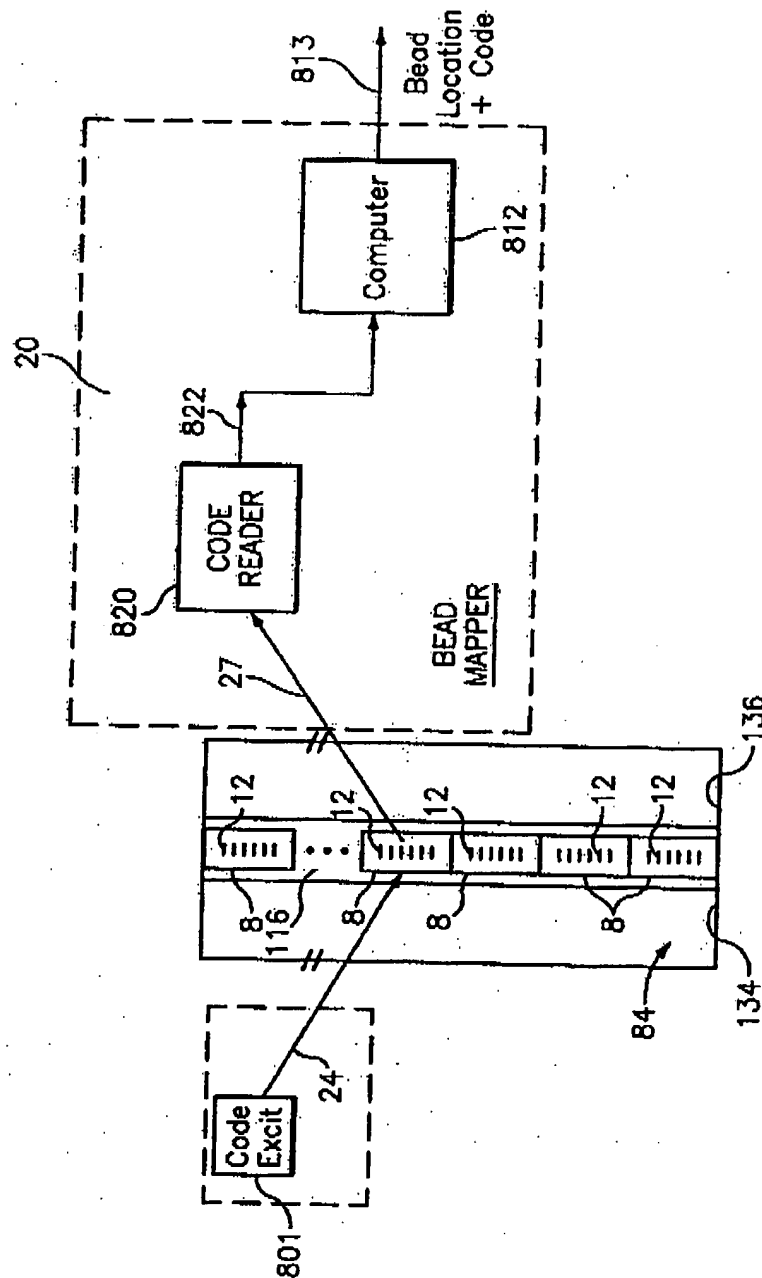


FIG. 6

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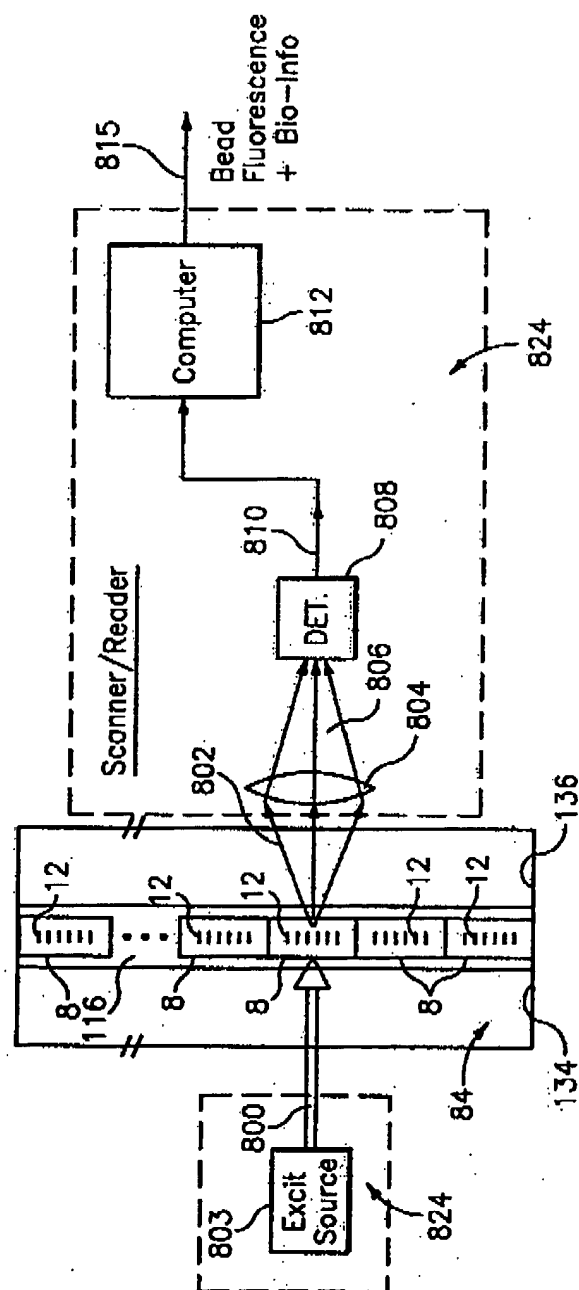
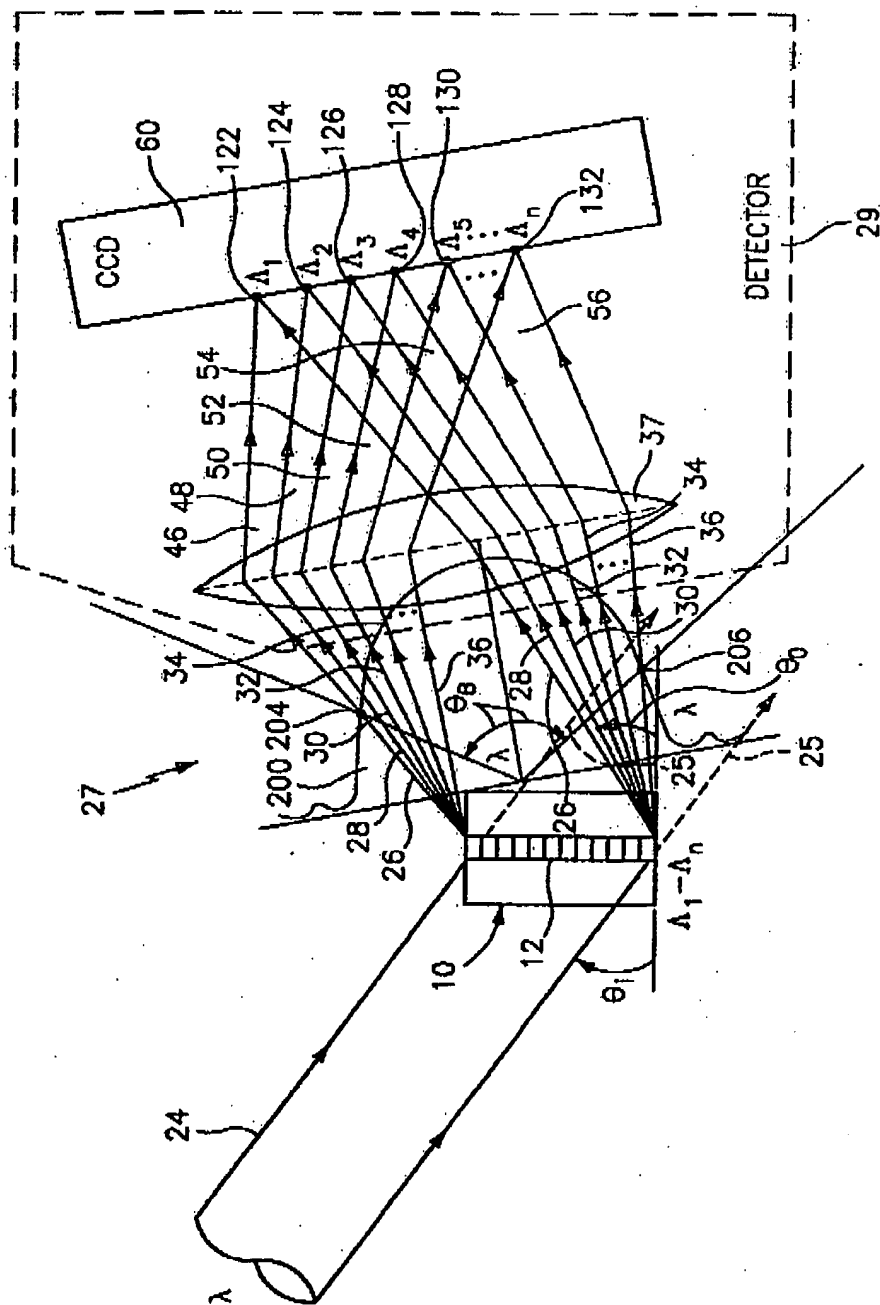


FIG. 9

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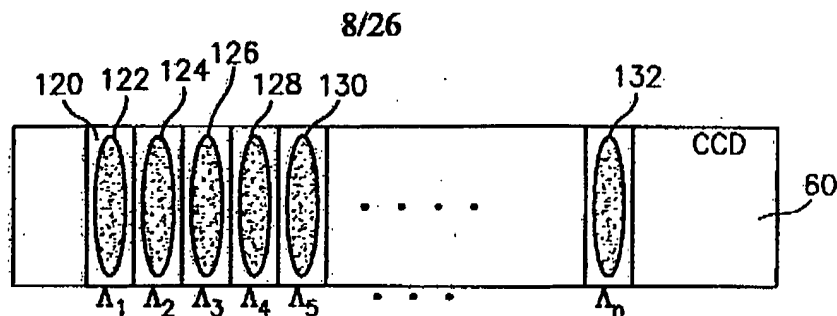


FIG. 11

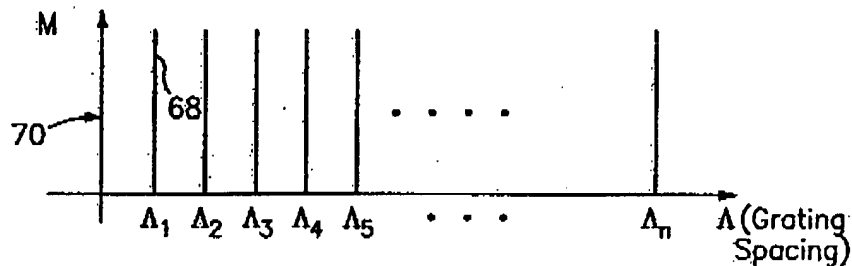
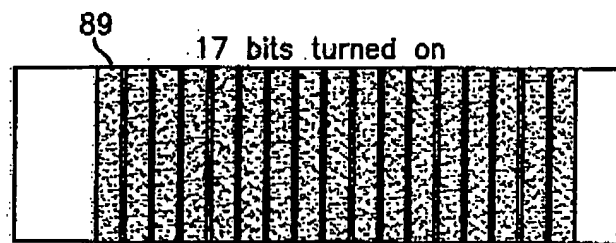
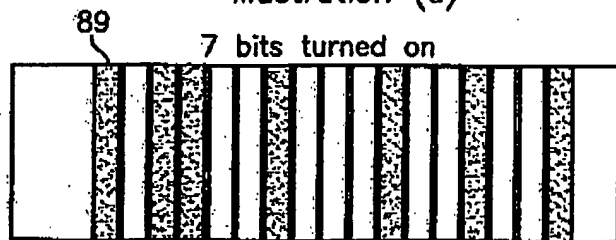


FIG. 12



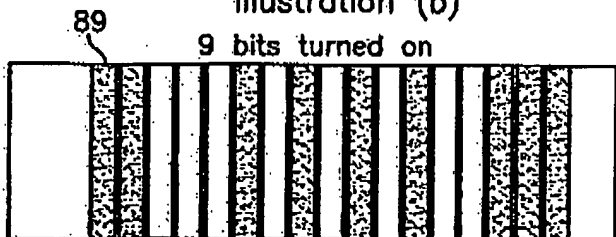
Bits: 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1

Illustration (a)



Bits: 1 0 1 1 0 0 1 0 0 0 1 0 0 1 0 0 1

Illustration (b)



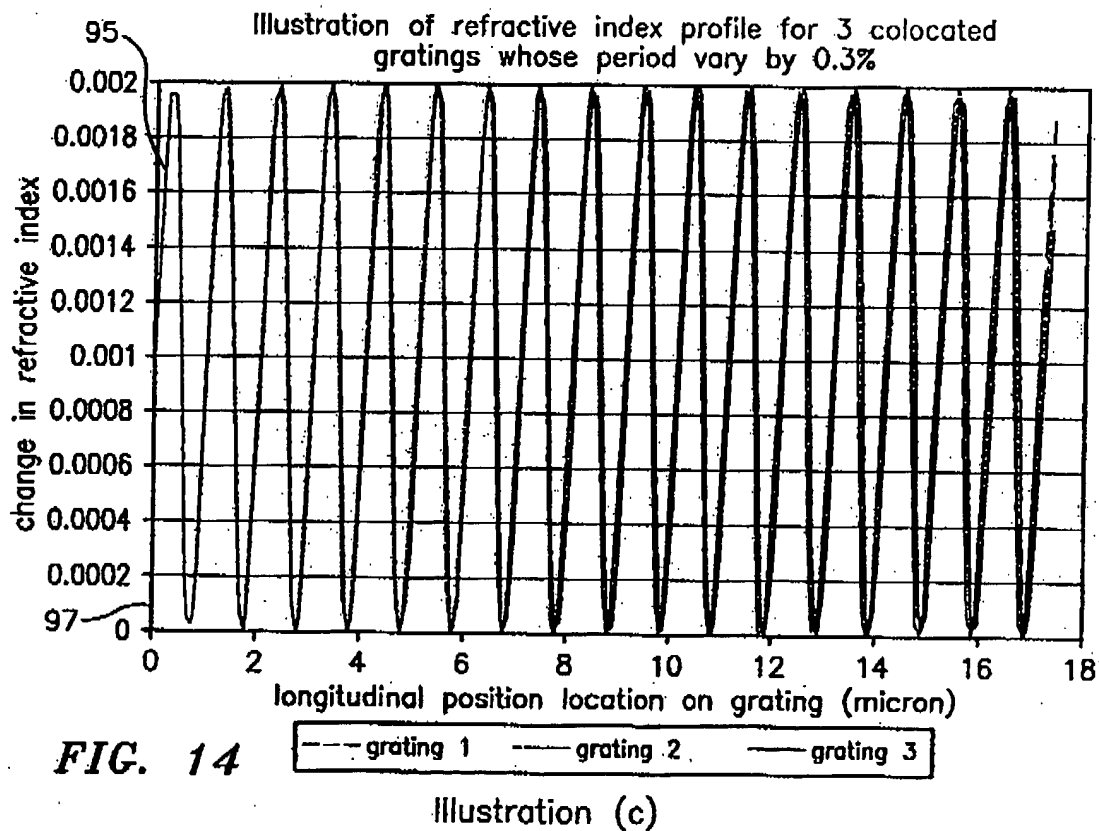
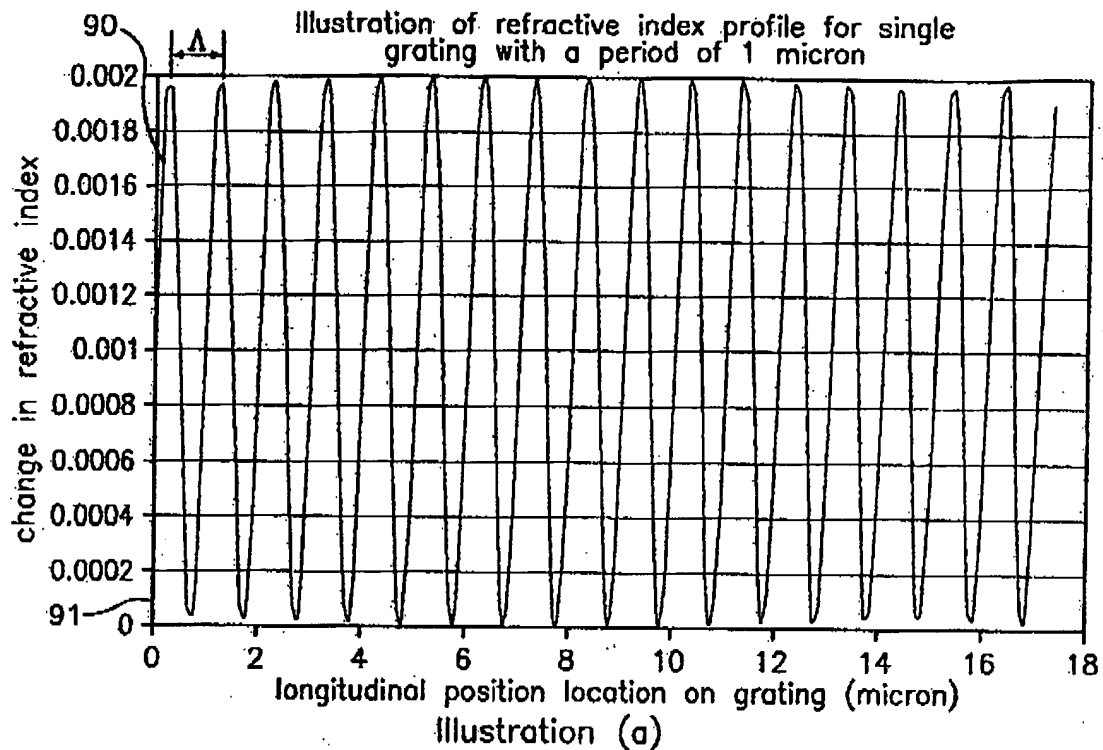
Bits: 1 1 0 0 0 1 0 1 0 1 0 1 0 0 1 1 1

Illustration (c)

FIG. 13



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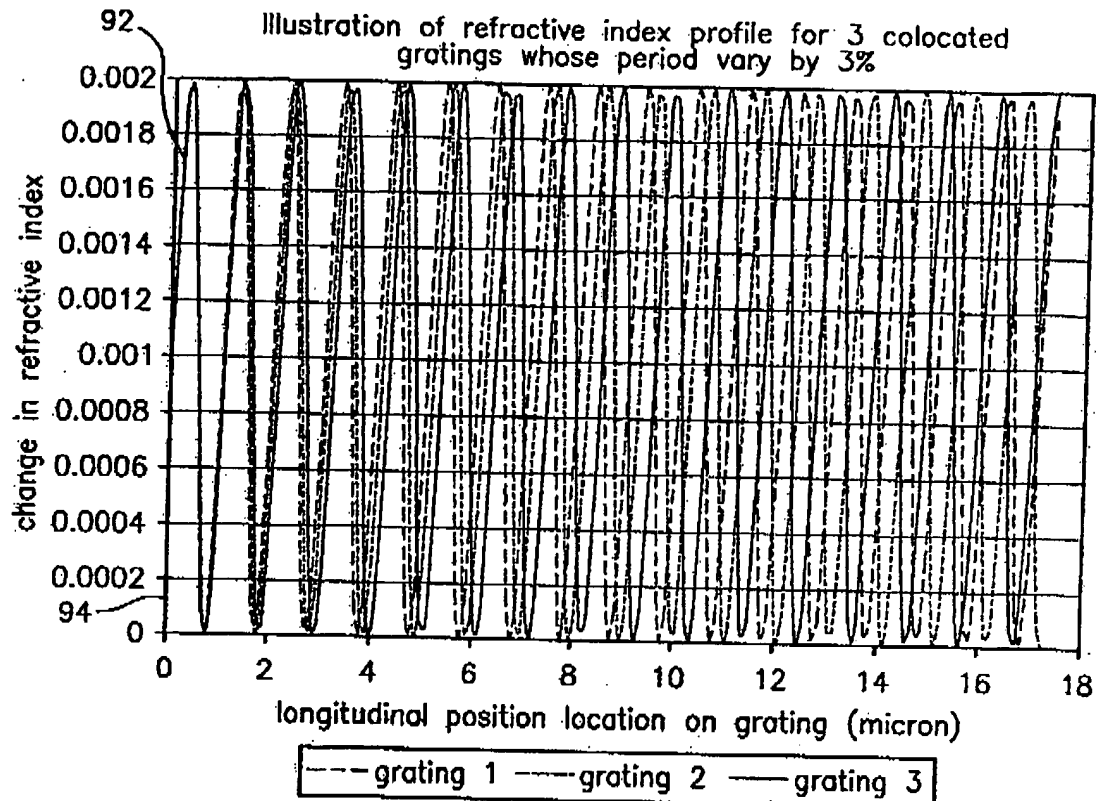


Illustration (b)

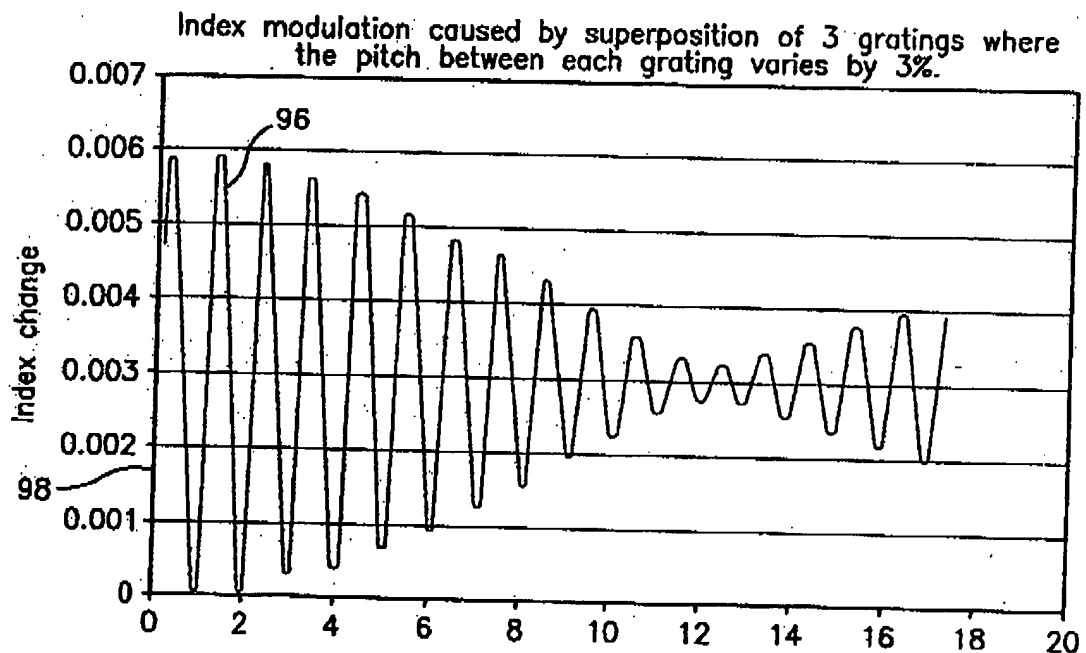


FIG. 14

Illustration (d)

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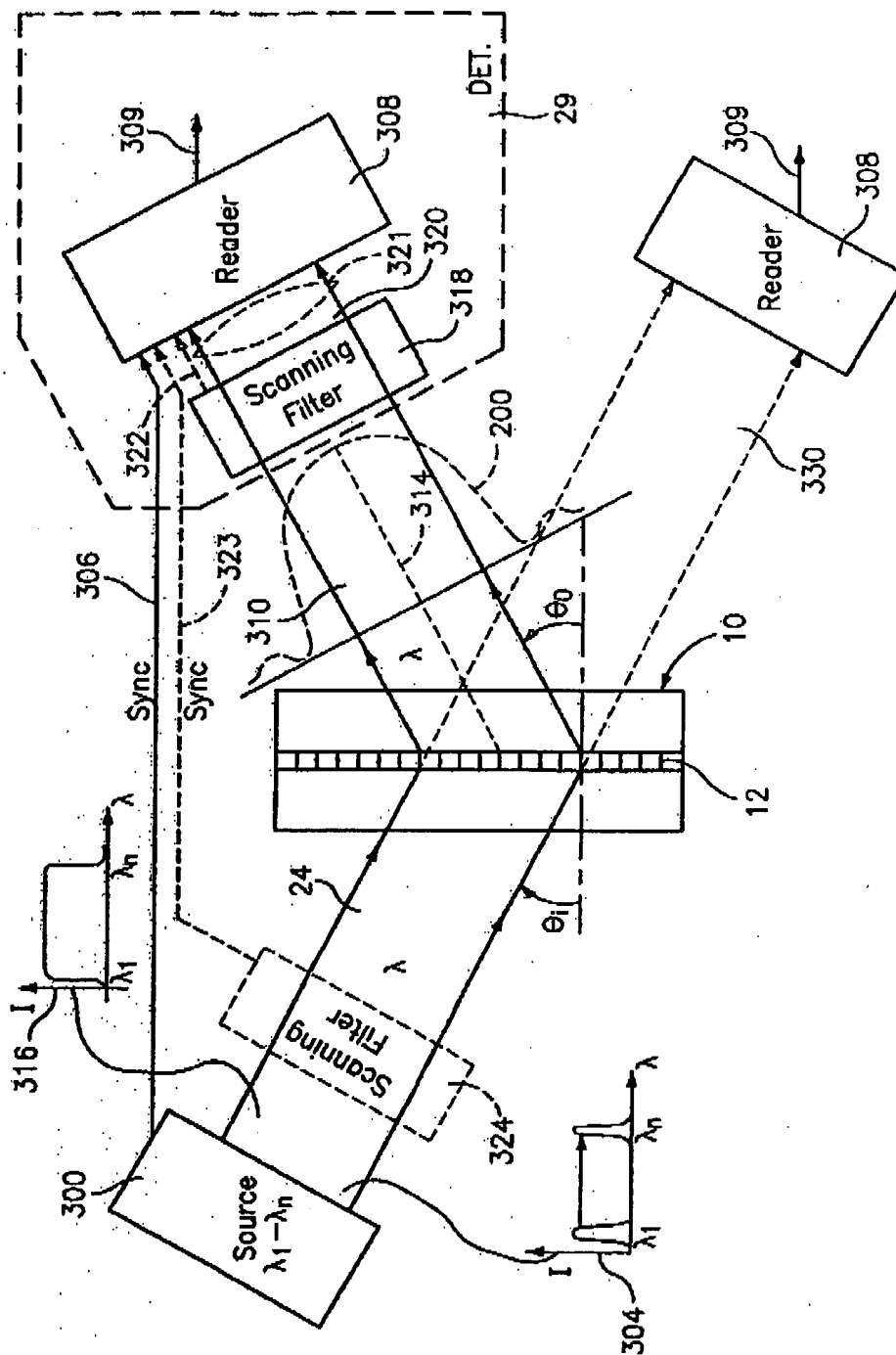
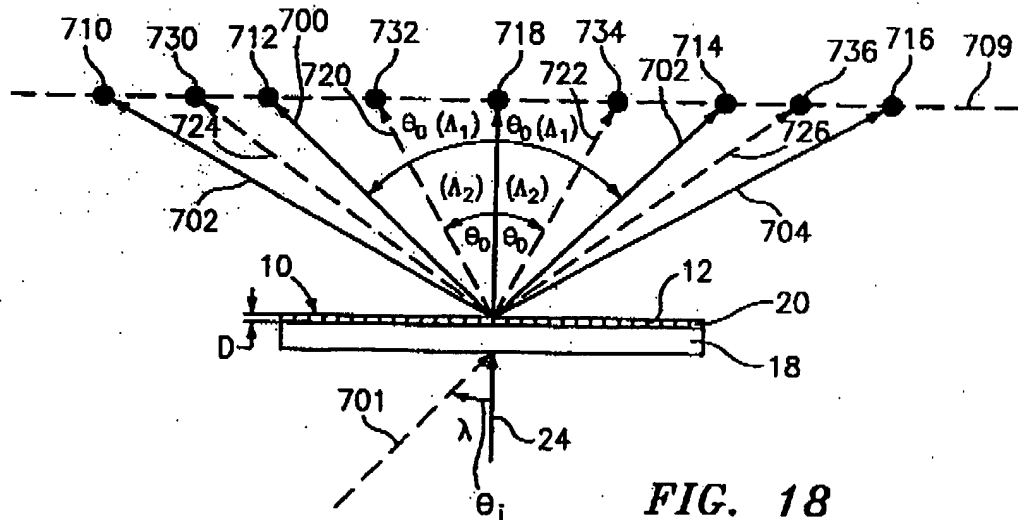
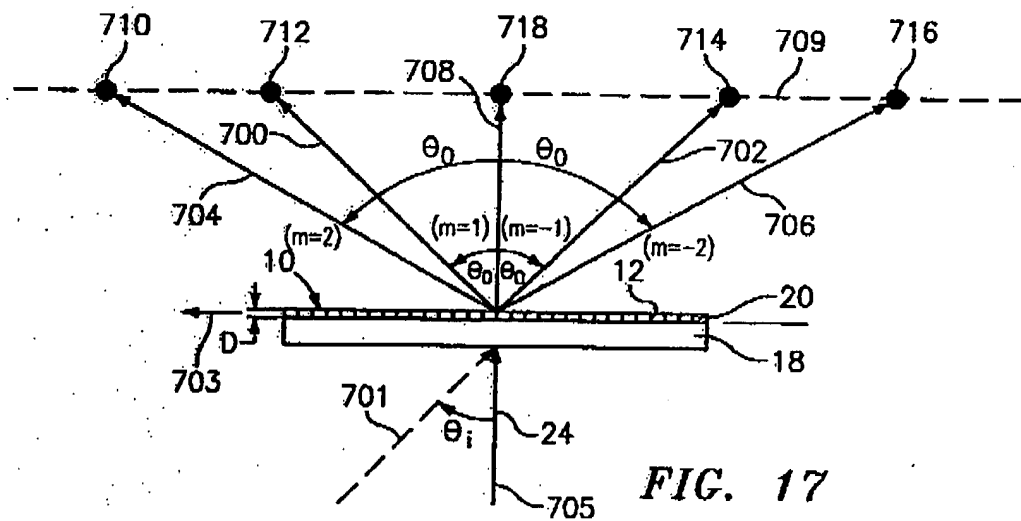
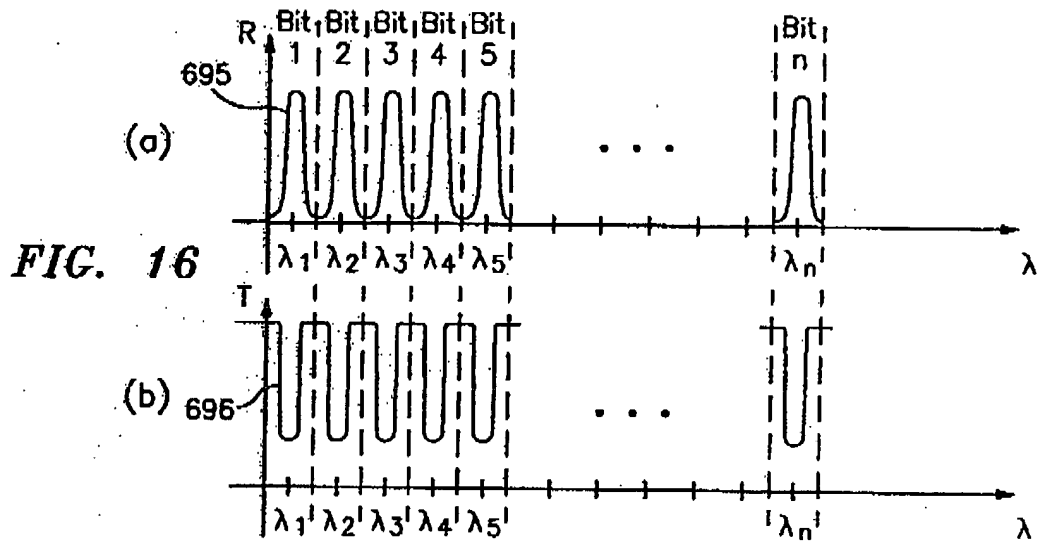
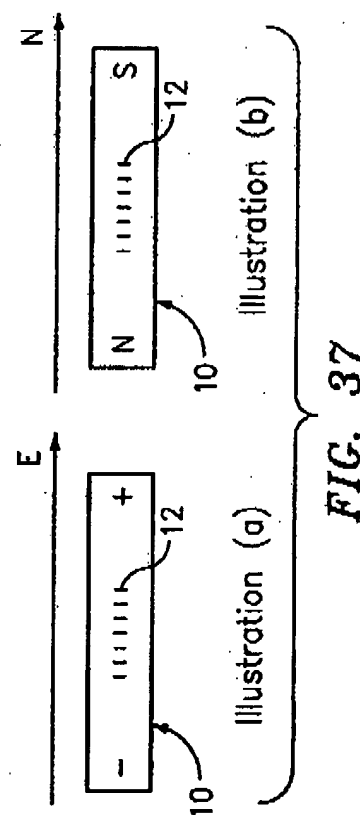
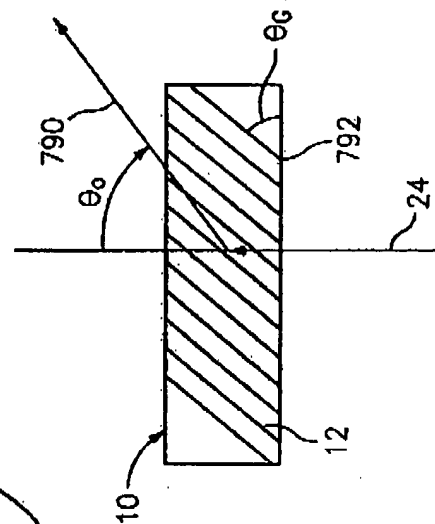
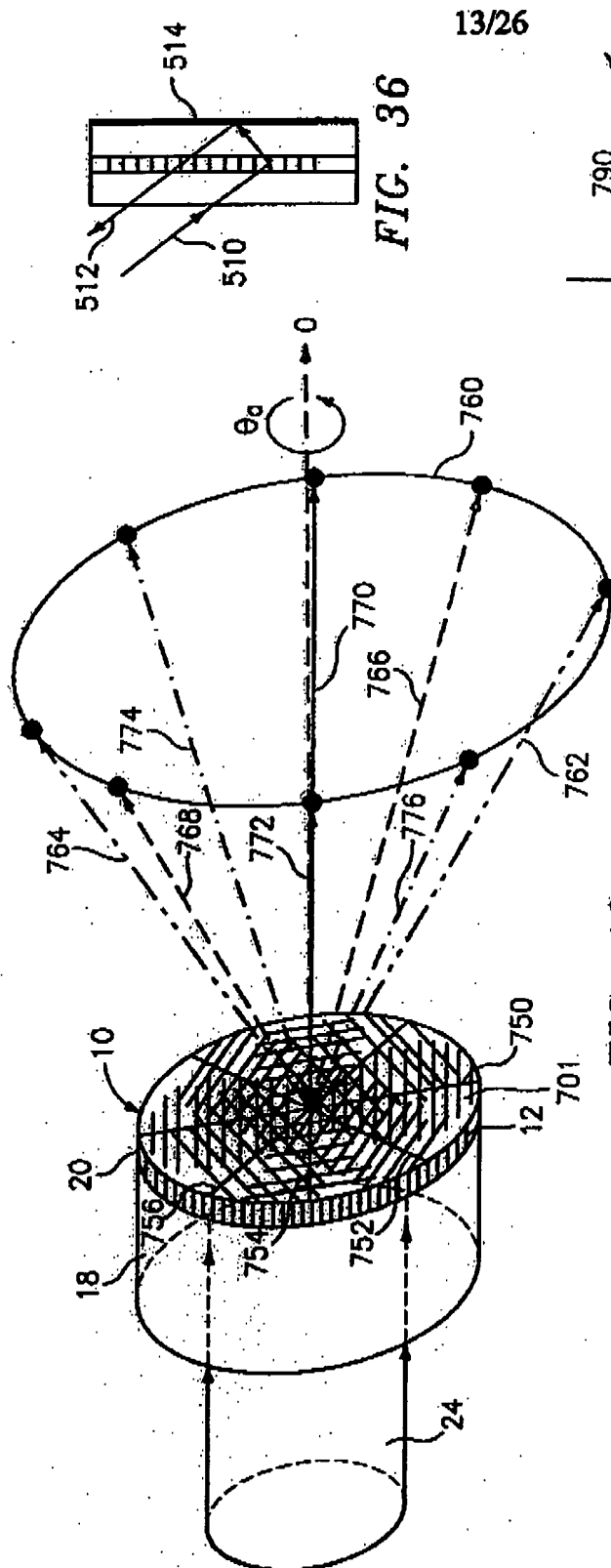


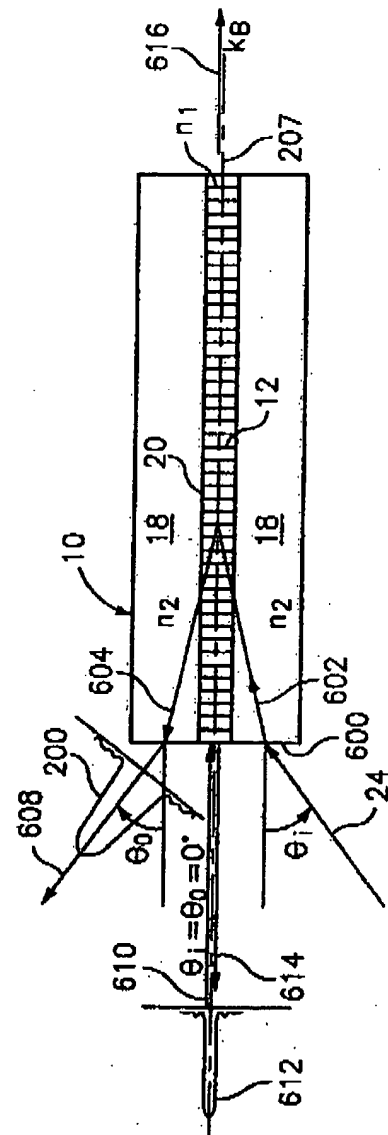
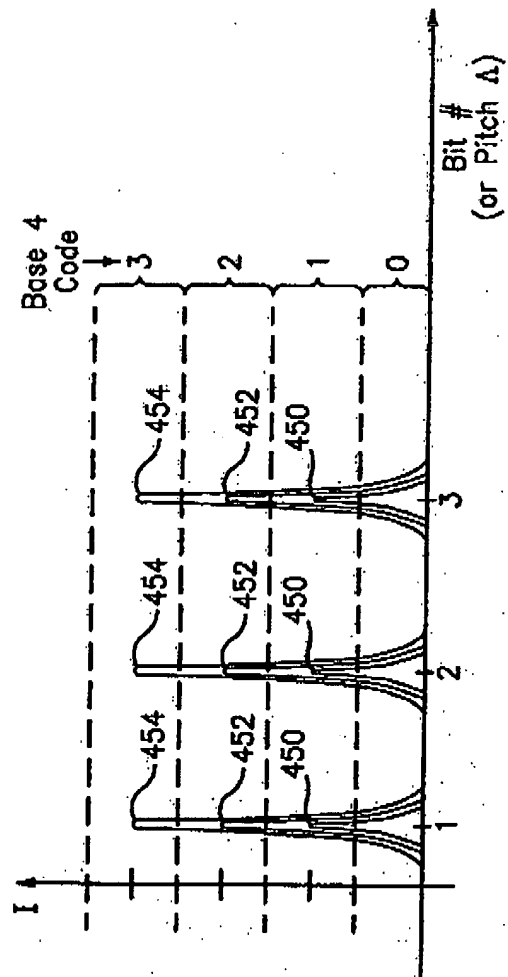
FIG. 15

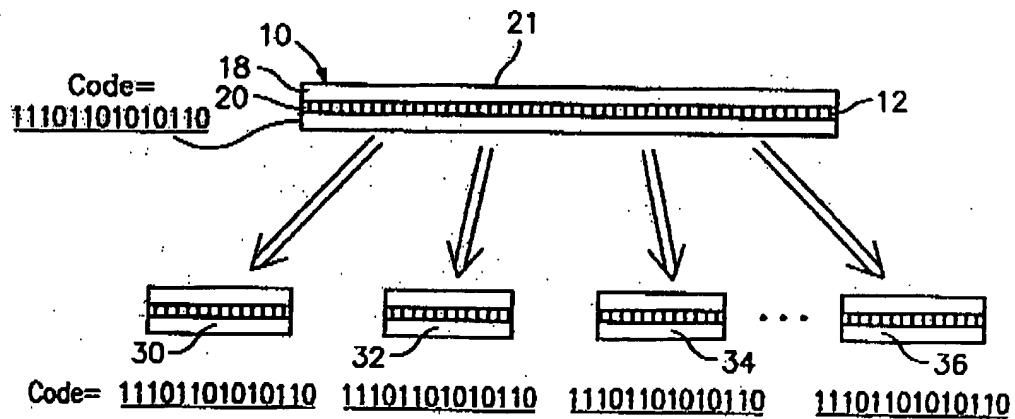
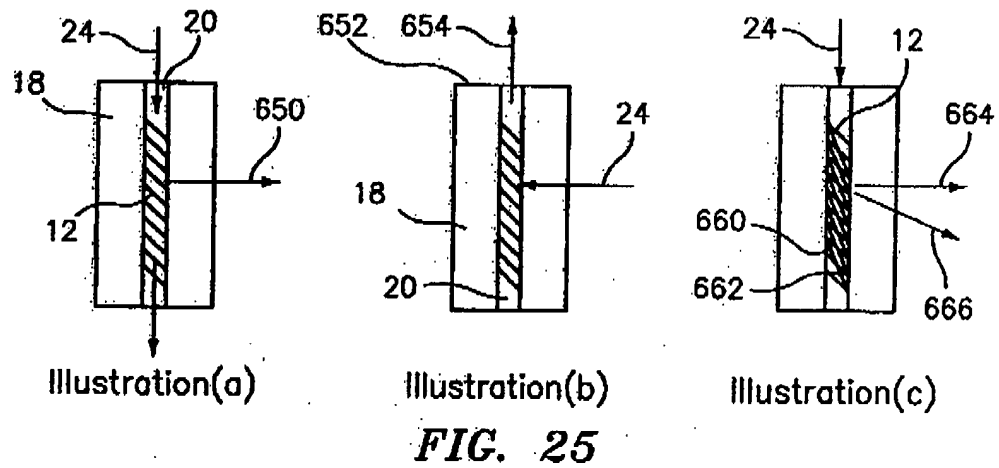
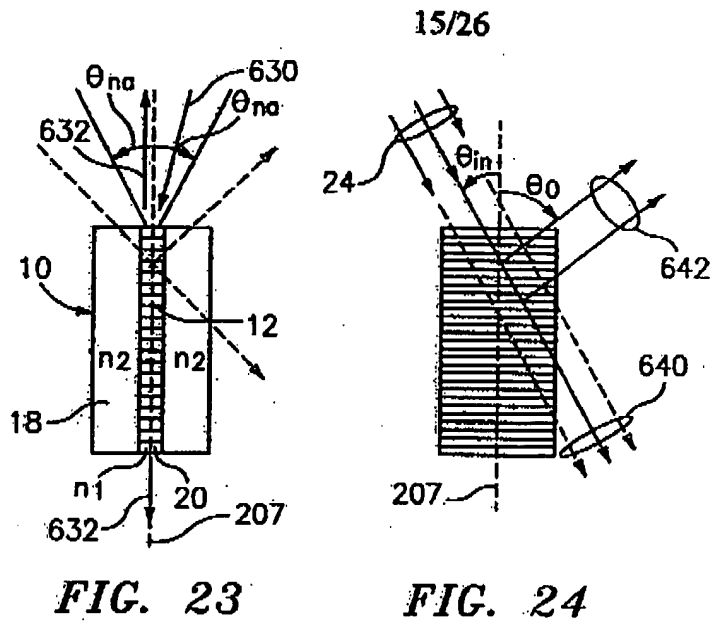
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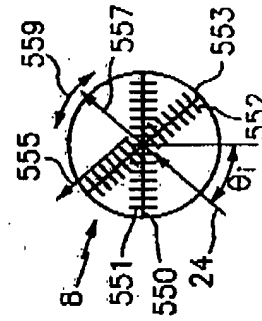
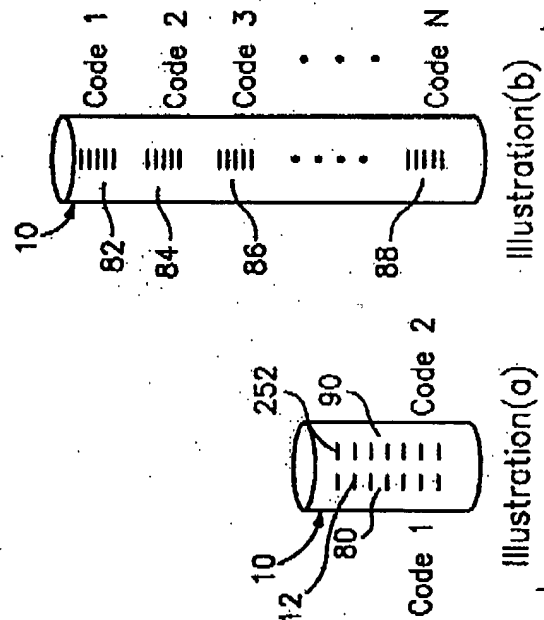
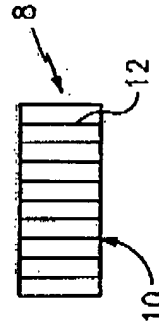
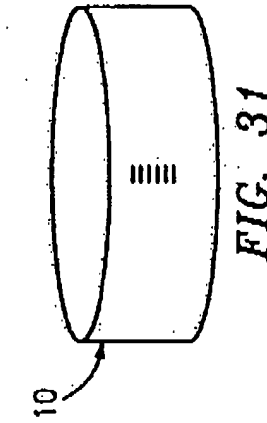
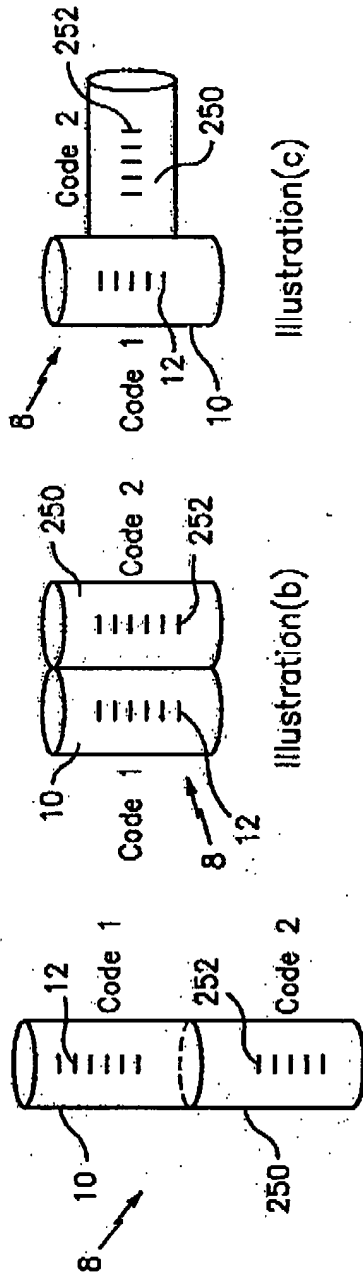


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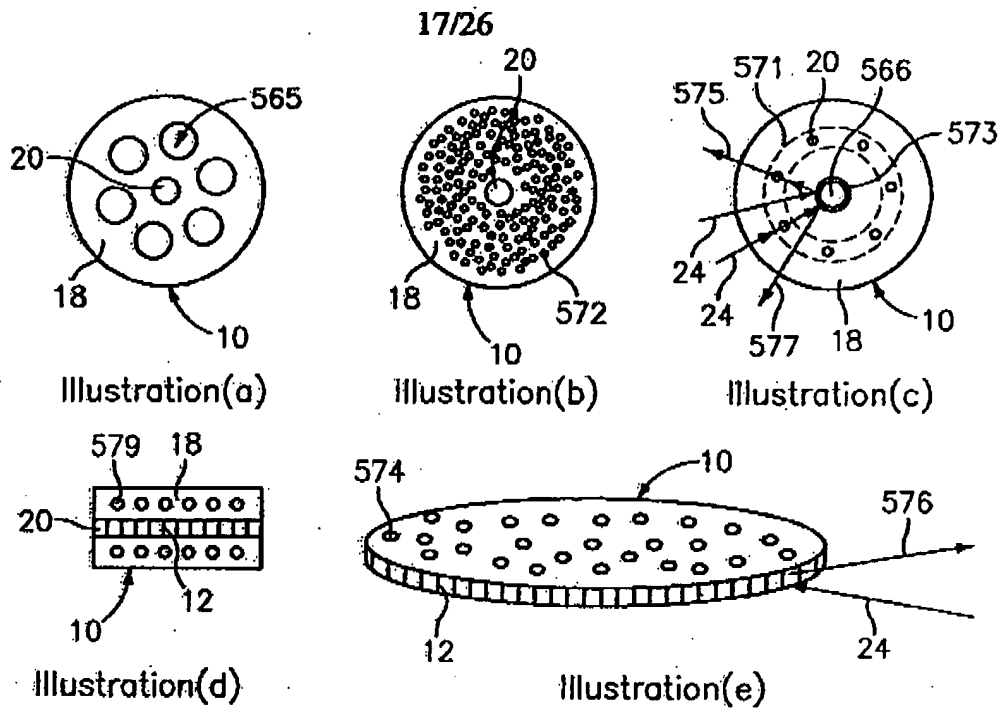
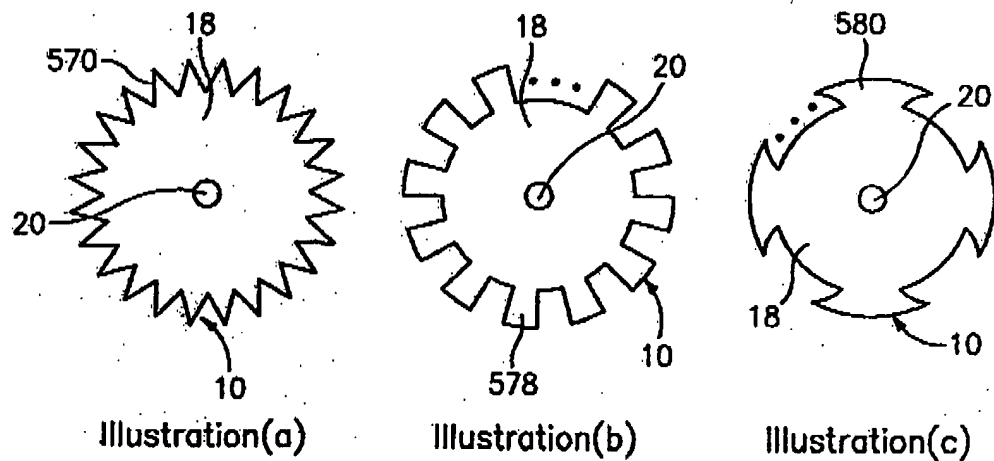
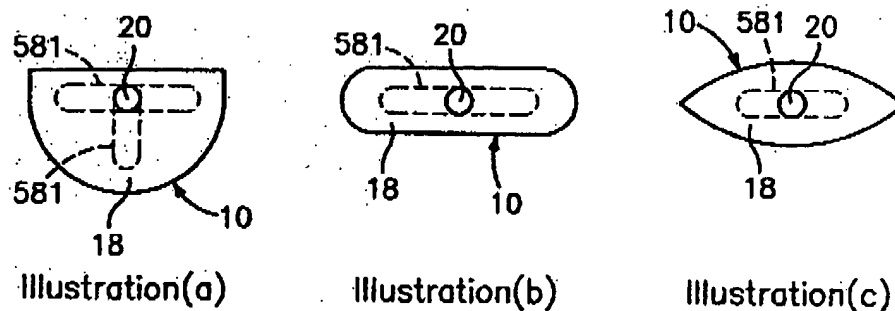




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**FIG. 33****FIG. 34****FIG. 35**

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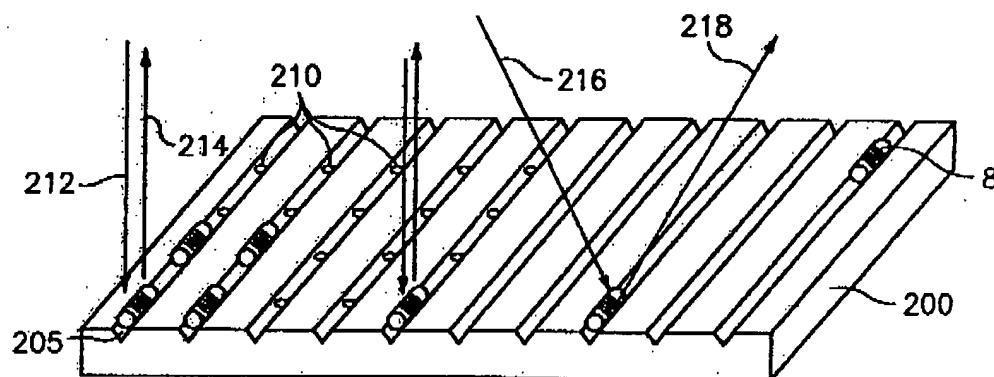


FIG. 38

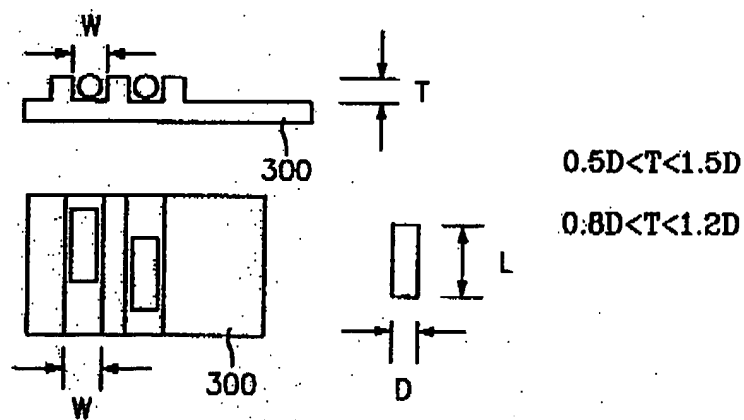


FIG. 39

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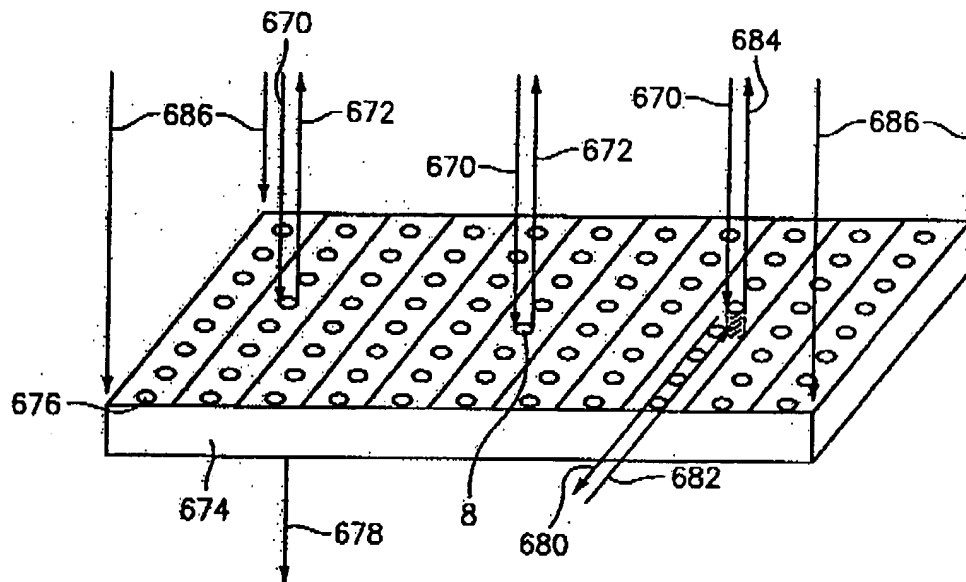


FIG. 40

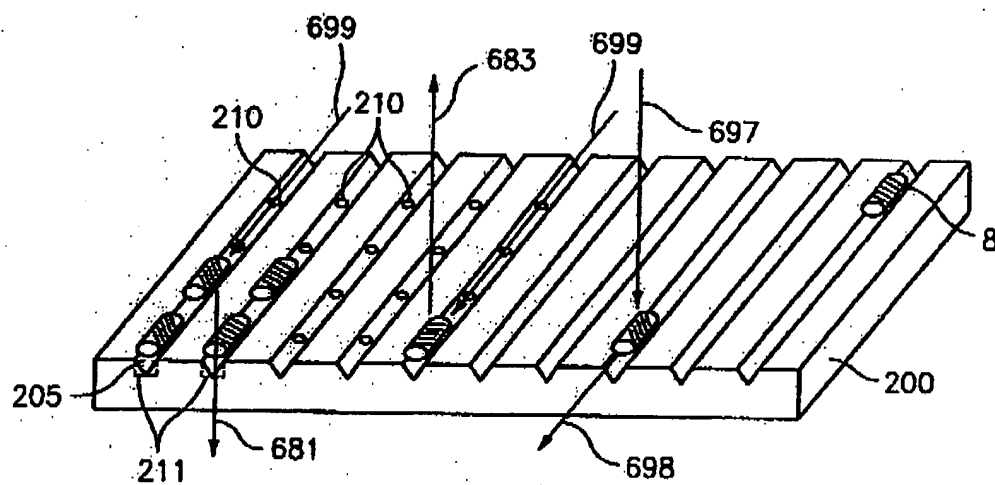
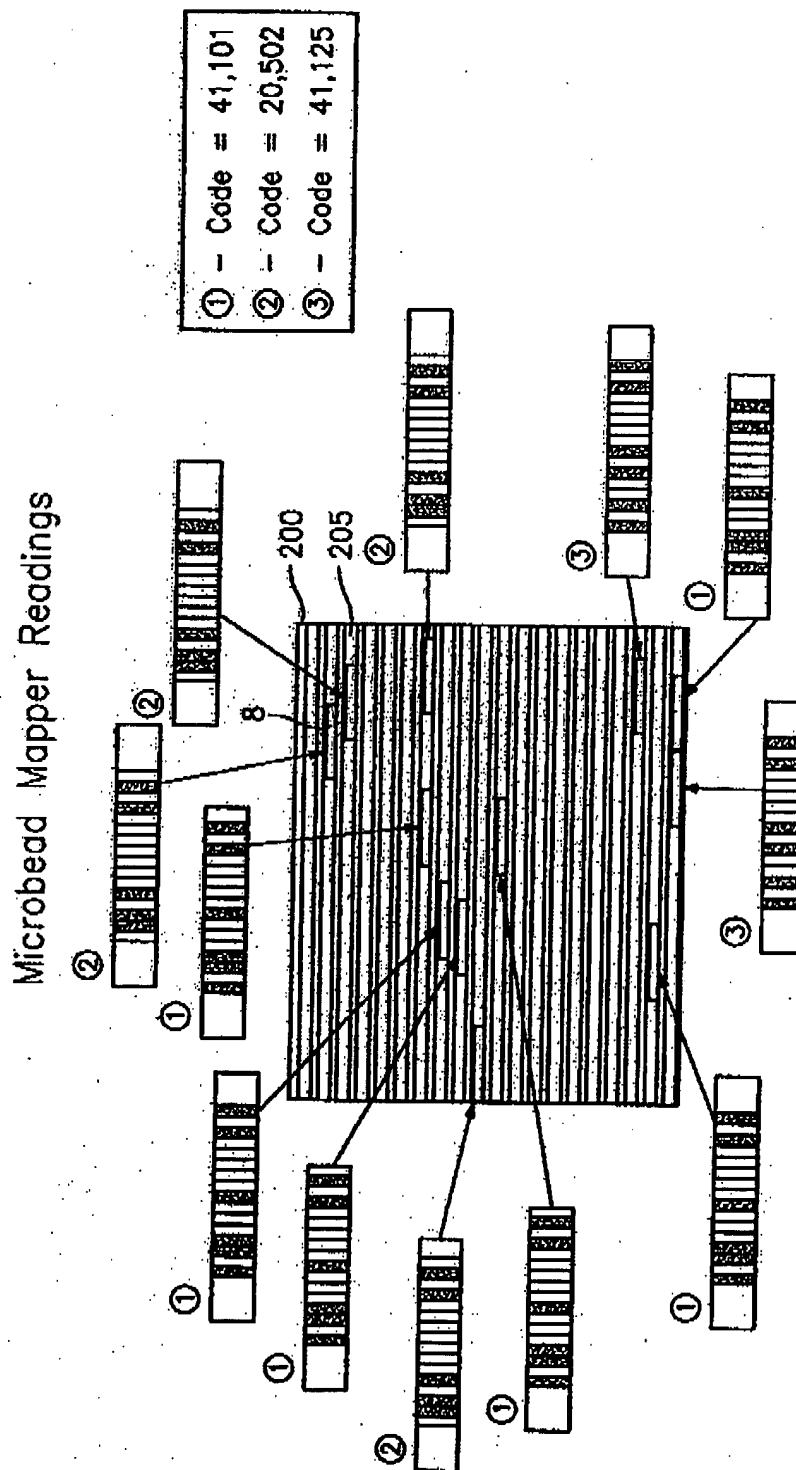


FIG. 41

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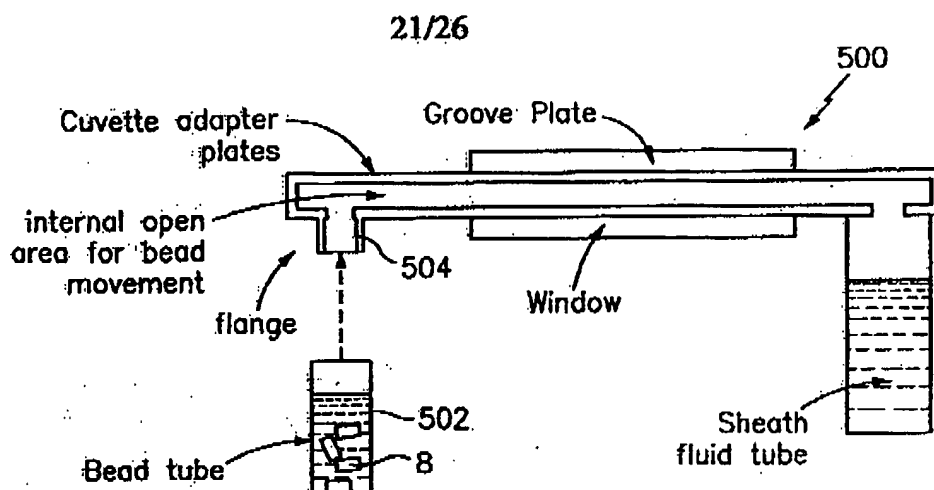


FIG. 43

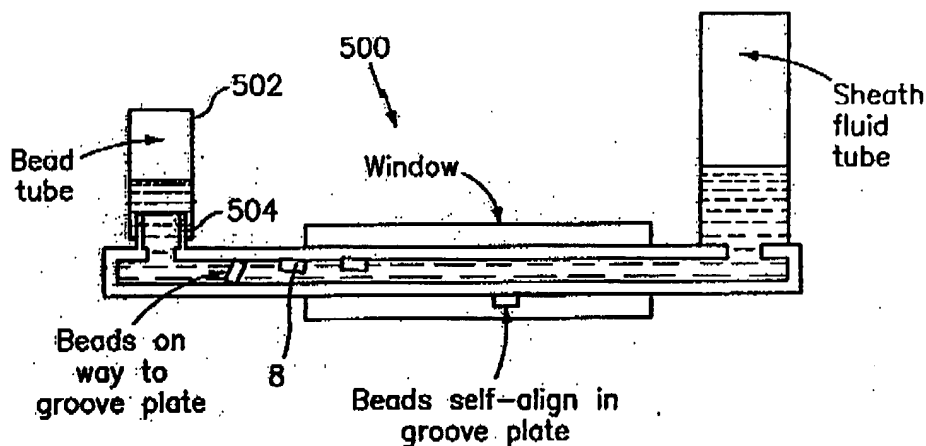


FIG. 44

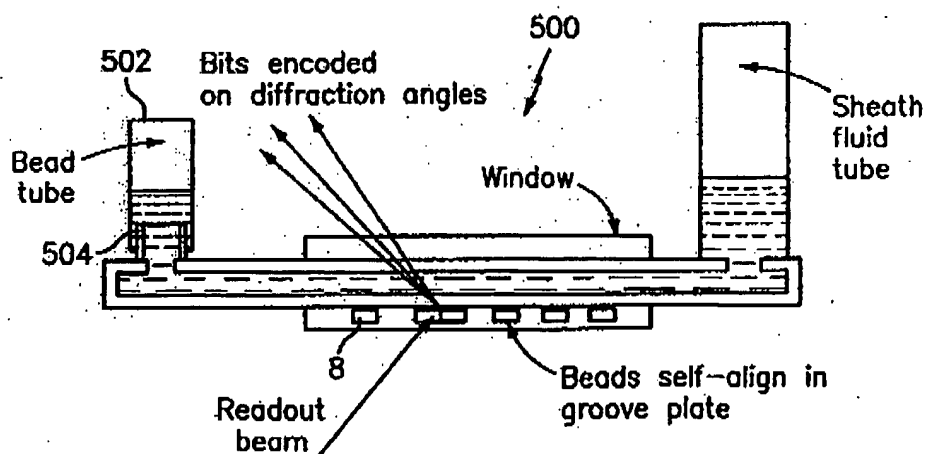


FIG. 45

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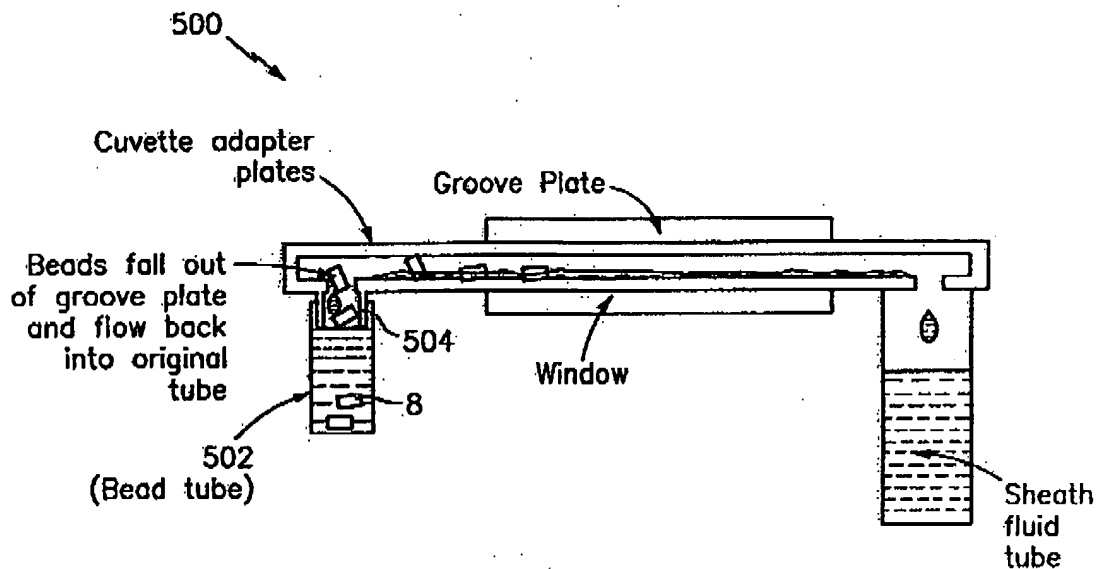


FIG. 46

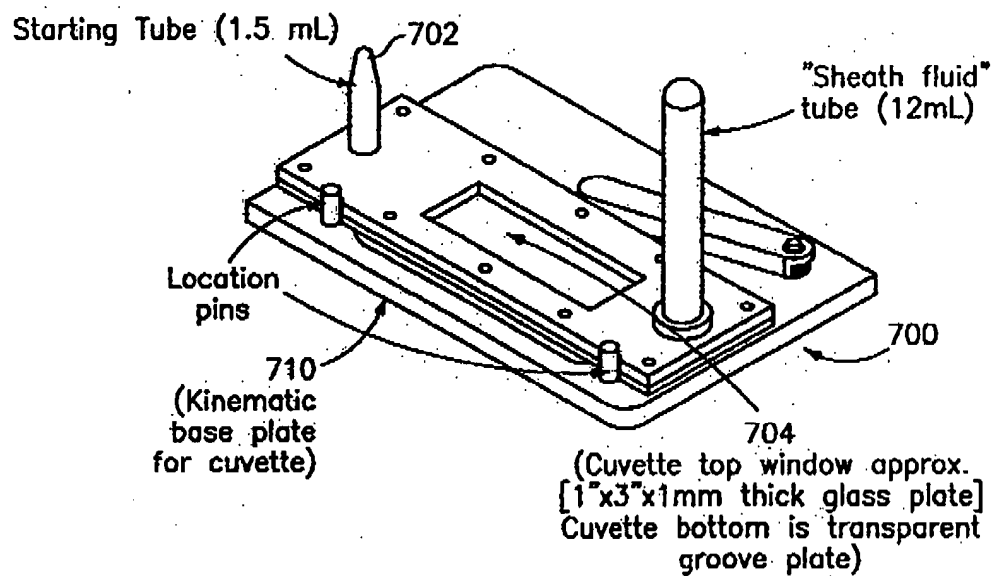
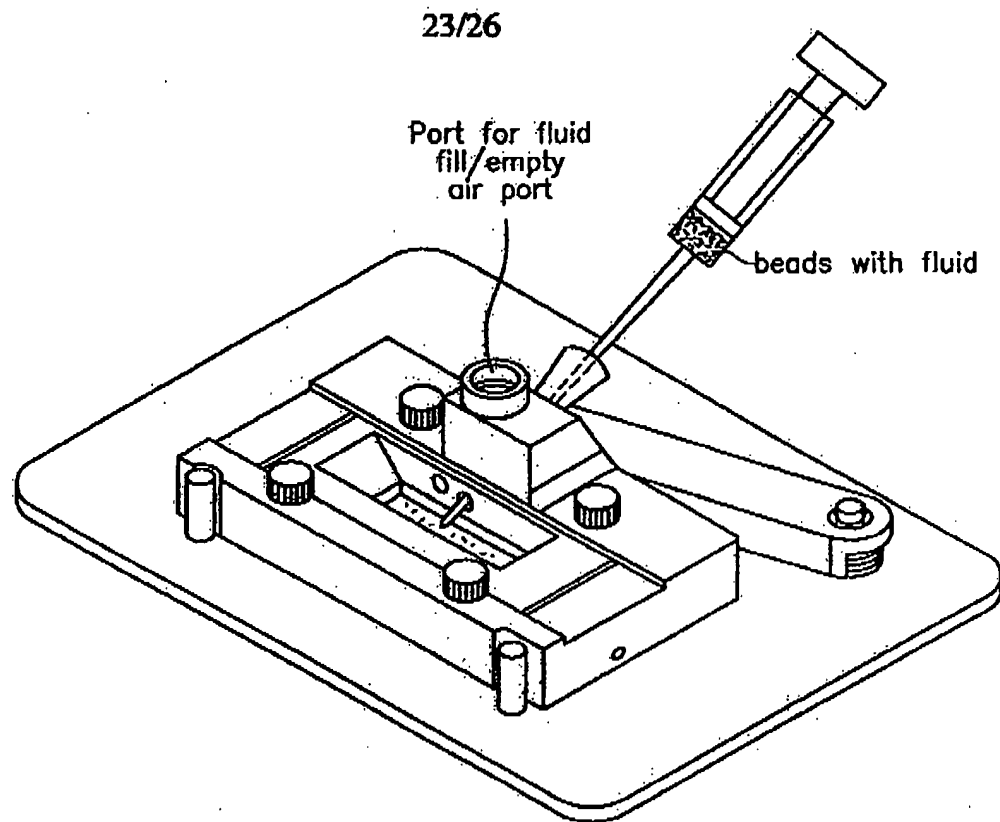
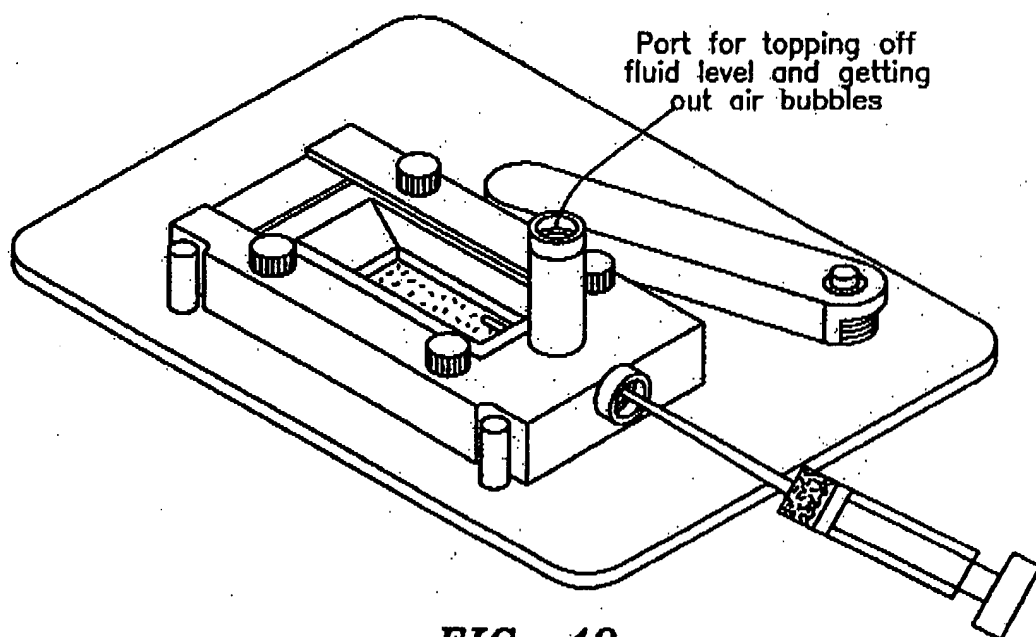


FIG. 47

**FIG. 48****FIG. 49**

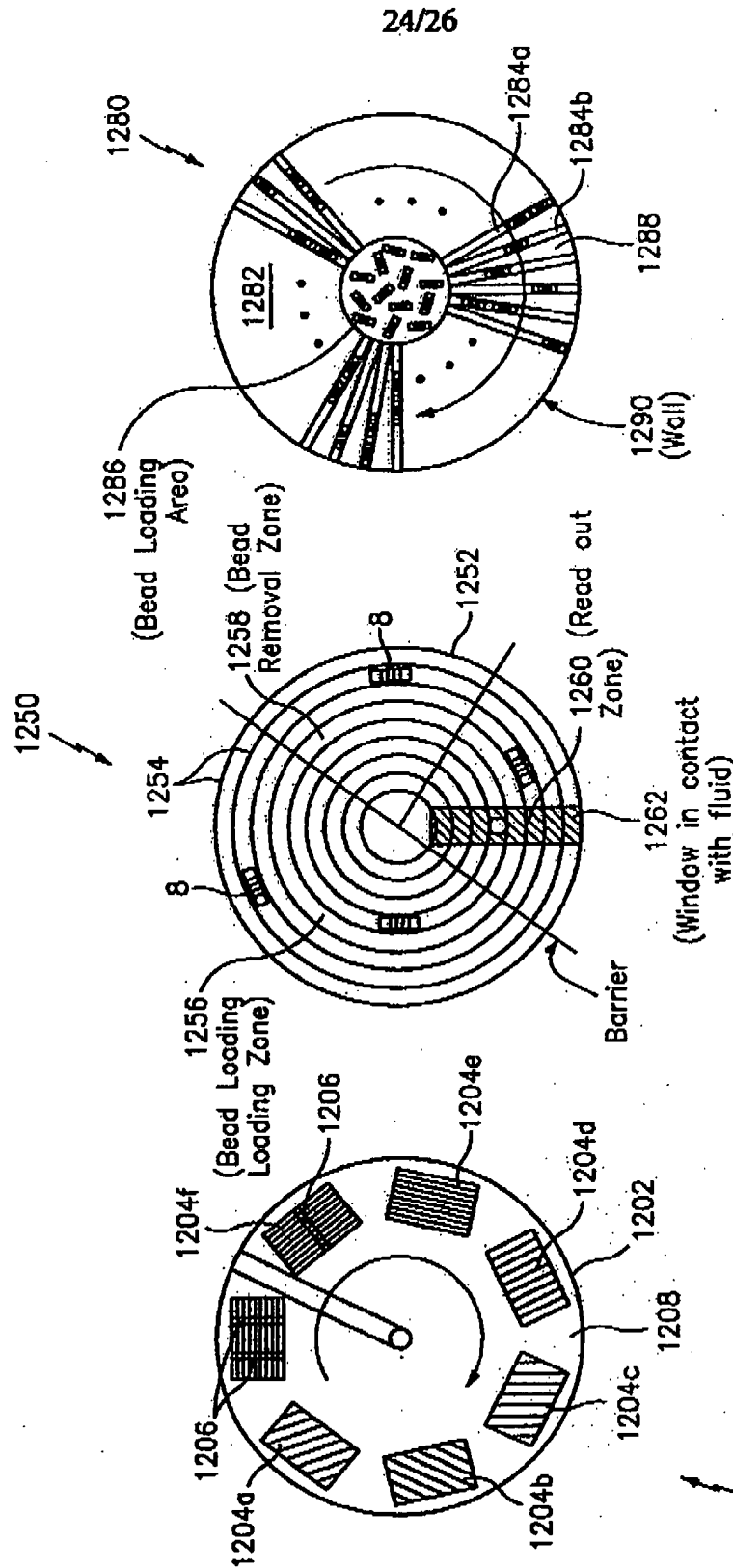


FIG. 50(c)

FIG. 50(a)

FIG. 50(b)





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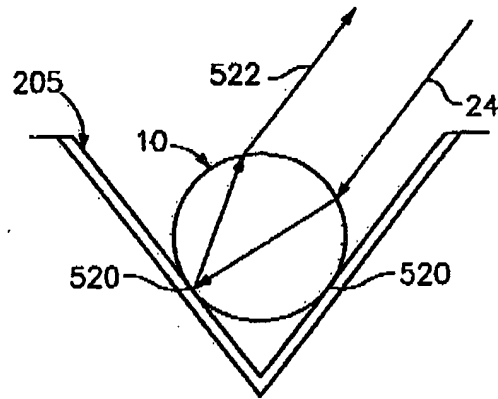


FIG. 52

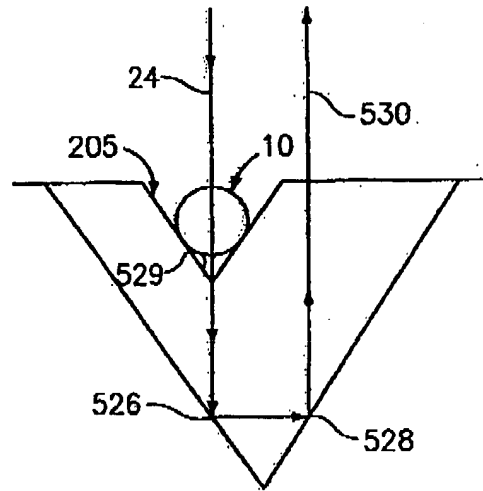


FIG. 53



FIG. 54

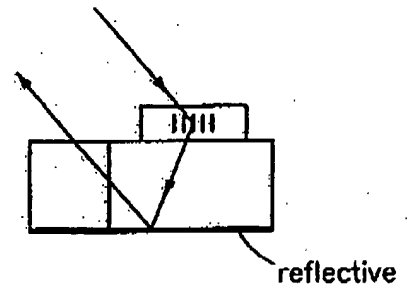


FIG. 55

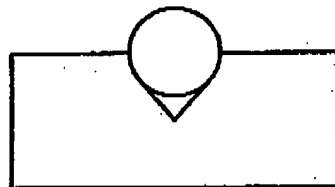


FIG. 56

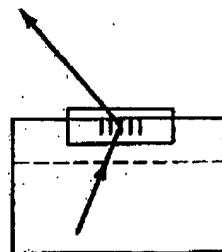


FIG. 57